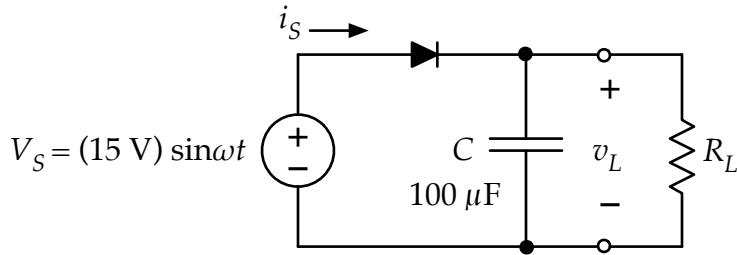


Use SPICE (pick your favorite flavor) to plot the load voltage and source current for the peak rectifier circuit shown below. Make three runs for $R_L = 500 \Omega$, $R_L = 2 \text{ k}\Omega$, $R_L = 5 \text{ k}\Omega$. The source frequency is 60 Hz. Include at least 3 periods in the time span for the simulation



Then use SPICE to determine the load voltage and source current for the rectifier circuit that includes a simple Zener regulator, as shown below. The Zener breakdown is at -10 V . Only one run is needed here, for $R_L = 1 \text{ k}\Omega$.

